

III B.Tech I Semester Supplementary Examinations, November 2006
MICROPROCESSORS AND MICRO-CONTROLLERS
(Electronics & Computer Engineering)

Time: 3 hours

Max Marks: 80

Answer any FIVE Questions
All Questions carry equal marks

1. (a) Draw the internal architecture of 8085? Explain about each block in it. [8]
(b) List out the assembler directives of 8086? And explain them? [8]
2. (a) What is the difference between a segment descriptor and a system descriptor? [8]
(b) What is the task state segment? How is it addressed? [3+5]
3. (a) Explain the floating point status register of 68000? [6]
(b) Explain the two parts in condition code register of 68000? How are the interrupts connected with the program status register. [16]
4. (a) Explain how pipelining improves the speed of operation? [6]
(b) What is score boarding and its use in RISC? [10]
5. (a) Explain the salient features of Pentium architecture. [6]
(b) Draw the schematic blocks of Floating Point Unit (FPU) of Pentium micro-processor and explain its different segments. [10]
6. (a) Explain the various stages involved in the development of Pentium based systems? [8]
(b) Explain the use of in circuit emulator in a development system? Discuss ICE for Pentium based system development? [8]
7. (a) What is meant by interrupt? What are the different interrupts of 8051? [6]
(b) Explain the interrupt operation of 8051 in detail? [10]
8. (a) Write a program of 8051 to copy the value 55 H into RAM memory location 40H to 45H using. [9]
 - i. Direct addressing mode
 - ii. Register indirect addressing mode without a loop
 - iii. With a loop
(b) Assume that ROM space of 8051 starting at 250H contains "Hello", write a program to transfer the bytes into RAM locations starting at 40H. [7]
