

**MAHATMA GANDHI INSTITUTE OF TECHNOLOGY**  
Gandipet, Hyderabad 500 075

**FACULTY PROFILE**

1. **JNTUH REGISTRATION ID** : 97150331-154428
2. **DATE OF BIRTH** : 07 12 1965
3. **DESIGNATION** : Professor & HOD
4. **YEARS OF EXPERIENCE** : 34 years
5. **EMAIL ID** : spsingh@mgit.ac.in, spsinghgahlot@gmail.com
6. **UG DEGREE** : BE (AMIE)
7. **PG DEGREE** : ME (SSP)
8. **PH.D(AREA OF THE PH.D WORK):** Radar Signal Processing
9. **SUBJECTS TAUGHT** : MW, DC, MPMC, DSP, EDC and CS
10. **PAPER PUBLICATIONS IN INTERNATIONAL JOURNALS** : 21
11. **PAPER PUBLICATIONS IN INTERNATIONAL CONFERENCES** : 08
12. **PAPER PUBLICATIONS IN NATIONAL JOURNALS** : 08
13. **PAPER PUBLICATIONS IN NATIONAL CONFERENCES** : 48
14. **BOOKS PUBLISHED:**
15. **RESEARCH PROJECTS UNDERTAKEN WITH NAME OF THE SPONSORING AGENCY** : 04
  - RPS (AICTE) : 01
  - IE(I) : 01
  - RCI : 01
  - DLRL : 01
16. **CONSULTANCY WORKS**
17. **RESEARCH GUIDANCE: (2+5=7)**
  - Research Scholars Guided : 02
  - Research Scholars Guiding : 05
18. **CONTRIBUTIONS AT THE DEPARTMENTAL LEVEL:**
  - (i) **Head, ECE Department MGIT since, March 2012.**
  - (ii) **Head of ECE Department event scheduling committee.**
  - (iii) **Head of ECE Department Academic council.**
  - (iv) **Head of ECE Departmental NBA committee.**



**19. CONTRIBUTIONS AT THE COLLEGE LEVEL:**

- a) Head, Examination and Academic section at MGIT for years 2010-12.
- b) Faculty in-charge of IEEE and IETE student Chapters/ society.
- c) Member of college Academic council.
- d) Faculty in-charge of students IEEE and IETE Chapters
- e) Convener, anti-ragging committee at MGIT till 2014
- f) Professor in-charge of library and information centre, MGIT since 2007.

**20. REFRESHER COURSES/ WORKSHOPS/CONFERENCES ATTENDED : 15**

- a) Attend one workshop on FPGA” , June 27, 2018
- b) Attend one day workshop on NIRF Ranking “Meltranking 2020” , May 23, 2018
- c) Attended One day seminar on Matlab and Simulink 2017
- d) Attended one day workshop on Mentor PCB System Form on 09 June 2017
- e) Attended One week in house training on “PLC, Embedded and E-CAD design” during 23/11/2015 to 28/11/2015.
- f) Attended One week in house training on “C& DS Programming” during 16/11/2015 to 21/11/2015.
- g) Attended One-day National Workshop on “Gender Sensitization” on 21.12.2015.
- h) Seminar on Matlab and simulink for engineering education 26 July 2013, Hyderabad.
- i) Attended Elsevier library connect seminar on 21 Nov 2013 at Hyderabad.
- j) One day workshop on Outcome based NBA 26 Oct 2013 at JNTU Hyderabad.
- k) Attended two days World Summit On Accreditation (WOSA-14) at New Delhi
- l) Attended International conference on Radar system IRSI-13, Bangalore, December-2013
- m) Attended 29th Convention of Electronics & Telecommunication Engineers, October 29-30, 2013.
- n) Attended Symposium on recent advances in telecommunications technology, ‘Tele Tech 13’ February 11<sup>th</sup> and 12<sup>th</sup>, 2013 SRM Chennai.
- o) Attended Seminar on Matlab and simulink for engineering education 31 august 2012, Hyderabad.

**21. WORKSHOPS/ CONFERENCES/SEMINARS/FDP's ORGANIZED : 27**

Twenty Seven number of short Term course, FDP, Student Technical Fest and Workshops are organized in the department either HOD or Convener of the events

S.No.	Name of the Event	Duration
1	Technical Fest MICROCOSM 2017	10-11 April 2017
2	Technical Fest MICROCOSM 2016	16 March 2016
3	Technical Fest MICROCOSM 2015	13-14 March 2015
4	MOBILE MAKING WORKSHOP	24-25 February 2015
5	Technical Fest MICROCOSM 2014	13-14 March 2014
6	Technical Fest MICROCOSM 2013	8-9 March 2013
7	Technical Quiz “Brain Strain 12”	26 March 2012

8	Technical Fest MICROCOSM 2012	1-2 March 2012
9	Technical Fest MICROCOSM 2011	17-18 March 2011
10	Technical Fest MICROCOSM 2010	6-7 April 2010
11	Technical Fest MICROCOSM 2009	Feb 2009
12	training programme on MW, DC, DICA and BS Lab	25-30 June 2018
13	Research Trends in Digital Image and Video Processing (AICTE Sponsored)	Two-week 4-16 December 2018
14	RF Antenna Design (Organized by E&ICT Academy Warangal & MGIT)	One week 19-24 June 2017
15	Workshop on IOT Using Raspberry PI	28, 30, 31 March 2017
16	training programme on DSP, EC, PDC & MPMC Lab	One week 21-26 November 2016
17	PCB making Workshop	14-15 October 2016
18	Recent Trends on Digital Image and Video Processing	22-27 February 2016
19	Workshop on Internet of Things	18-19 February 2016
20	FDP on Digital Design Using Verilog & VHDL	15-19 December 2014
21	Training Programme on MPMC Lab	One week 12-17 May 2014
22	Workshop on 6 <sup>TH</sup> Sense Botz	7-8 March 2014
23	Workshop on Human Robot Interaction	22-23 February 2013
24	FDP on E-CAD and VLSI Design	6-8 Sep 2012
25	Workshop on X Robosenso	27-28 Feb 2012
26	Workshop on DSP Processor (DSPP 2012)	20-21 January 2012
27	Android Mobile Operating Systems	4-5 November 2011

## 22. INVITED LECTURES DELIVERED

: 11

- a) **Delivered a Lecture on “Microprocessor and Applications ” in Induction programs for young engineers at Midhani on 22, March 2018**
- b) **Delivered a Lecture on “ Advances in Wireless Communications and applications ” in one week faculty development program at CMRCET, 14 December, 2017**
- c) **Delivered a Lecture on “ Advances in Digital Communications and applications” in one week faculty development program at CMREC, 24, May 2017**
- d) **Delivered a Lecture on “Radar Signal Detection in Noise and Radar Cross Section” for Faculty Development Program RSP-16, organized by NERTU, Osmania University on 27 July 2016.**

- e) Delivered Expert lecture on Advances in control System at Aroura College of Engg September, 2016
- f) Delivered Expert lecture on Advances in ADSP and applications in FDP at Anurag College of Engg , May, 2017
- g) Delivered Expert lecture on Multi Rate Signal Processing and applications at at Anurag College of Engg , December, 2016.
- h) Delivered Expert lecture on Advances in control System and applications at in two week AICTE Funded FDP at MCT department , MGIT, May, 2015
- i) Delivered Guest/ invite lecture on Current and future trends in Radar systems at Mall Reddy institute of Technology, Hyderabad on 03 Aug 2013,
- j) Delivered Expert lecture on microprocessors and microcontrollers and Applications at Midhani in Induction program for young engineers at Midhani on 06 Jun 2013
- k) Delivered Guest/ invite lecture on Current and future trends in Radar systems at One day seminar at K G Reddy College of Engineering and Technology, Hyderabad , on 3 April 2013

### 23. MEMBERSHIP OF PROFESSIONAL BODIES

- (i) Life Fellow member of IETE,
- (ii) Life Fellow member of IE (I),
- (iii) Life member of ISTE
- (iv) Senior member of IEEE.

### 24. HONORS/AWARDS:

- (i) Received best technical instructor award at MCEME.
- (ii) Received twice cash award for indigenization work on the Radar 'Schilka'.
- (iii) Received cash award for modification in radar 'SFM'.

### 25. ANY OTHER CONTRIBUTION: Some of the contribution are listed below:

- a) Published 86 research papers in Journals/ Conferences(National & International)
- b) Successfully guided two and presently guiding four research scholars for Ph.D under JNTUH.
- c) Facilitated 10 (Ten) M.Tech students to complete projects effectively.
- d) Productively guided 45(Forty five) B.Tech projects.
- e) Successfully Completed AICTE Research project (RPS) on MIMO Radar.
- f) Member of PMRC for DLRL Project 'HIMRAJ'
- g) Technical expert member Improvement of RLG performance using wavelet and S-Transform, RCI project at CBIT, Hyderabad
- h) Member, Technical expert of DLRL Project on Design of LPI Algorithm executed at Department of ECE , OU.
- i) Member of Indigenized team who Indigenized valve-based circuits of Schilka Radar (Under the indigenization programme)
- j) Effectively made several modifications to reduce error rate in the radar systems.
- k) Developed new test equipment to substitute the old servo technical test system of Anti- aircraft gun, L-70.
- l) Developed electronic sweeping De-sulphater circuit to enhance the life of Lead acid battery.
- m) Developed Synchro - based training model.

- n) **Member of several Technical Committees of National/ International Conferences.**
- o) **Intelligent traffic light technology, R & D Project of IE(I).**
- p) **Technical Reviewer of following reputed Journals:**
  - (i) **IEEE on Electronics devices.**
  - (ii) **IEEE on Electronics devices.**
  - (iii) **ISTE Journal**
  - (iv) **IET on signal processing, UK.**
  - (v) **Journal of IE (I), India.**

**86 Papers are published in Journals and conferences (National/ Internationals bases)**

**(a) Eight Papers are Published in National Journals ( S.No 1 -8)**

- [1] **“Polyphase radar signal design using Modified simulated Annealing Algorithm”** IETE Journal of Research, Vol-53, No. 2, ISSN: 0377-2063, pp 173-182, March-April, 2007.
- [2] **“Binary Sequences Design** Journal of Technology and Engineering Sciences(JTES) Delving;, Vol 1, No. 1 January –June 2009, ISSN:0975-5829, pp 20-25
- [3] **“Thirty-Two phase sequences with good Autocorrelation properties”** *S`adhan`a* Vol. 35, Part 1, ISSN: 0256-2499, February 2010, pp.63-73. © Indian Academy of Sciences India.
- [4] **“Sixteen Phase Sequences Designed using MSAA”** IE Journal of CSE, Vol 91, ISSN: 2250-2106, May 2010, pp 29-34, India.
- [5] **“Discrete Frequency Codes Design With Good Correlation Properties”**, Journal of Technology and Engineering Sciences (JTES) Delving;, Vol -1, No-2, ISSN:0975-5829, pp 64-69, Jul-Dec 2009.
- [6] **“Sixty-phase Sequences Design with Good Autocorrelation Properties”**, IETE JOURNAL OF RESEARCH | Vol 57 | ISSUE 3 |, May-jun 2011, ISSN: 0377-2063, pp -250-256
- [7] **“Binary Codes Design with Good Merit Factor”** accepted in Journal, JRRT-2011, ISSN 2250 – 3943 (Print)., Feb 2012.
- [8] **An approach for the segmentation of satellite images using moving KFCM and Naïve Bays Classifier”** Journal on Electronics Engineering, Vol. 3 | No. 2 | December 2012 - February 201, pp 7-15.

**(b) Twenty One papers are Published in International Journals( S.No 9 -29)**

- [9] **“Polyphase sequences with good correlation properties”** International Journal of Systemics, Cybernetics and Informatics pp. 26-30, April, 2007.
- [10] **“Discrete Frequency Coded Radar signal Design”** IET Journal of signal processing U.K., Vol 3, issue 1,Print ISSN 1751-9675, Online ISSN 1751-9683, pp 7-16, 2009.
- [11] **“Eight -phase Sequence Sets Design for Radar”** WSEAS Journal, Greece, ISSN: 1790-5022, pp-95-105, Issue 3,Vol -5, March 2009.
- [12] **“Binary Sequences Design Using MGA”** International Journal of Computational Intelligence Research, ISSN 0973-1873 Volume 6, Number 4 (2010), pp. 531–540
- [13] **Adaptive Region Segmentation In Satellite Images For Urban And Non Urban Region Detection”** International J. of Engg. Research & Indu. Appls. (IJERIA),ISSN 0974-1518, Vol.4, No. IV (November 2011), pp. 149-164
- [14] **“One Twenty Phase Code design for Radar”** International Journal of Advanced Computer Research (ISSN (print):2249-7277, ISSN(online):2277-7970) Volume-3, Number -4, Issue-13,December-2013, pp- 232-238.

- [15] **“Hybrid Clustering Algorithm and feed-Forward Neural Network for satellite Image Classification”** International Journal of Engineering Science Invention ISSN(online):2319-6734, (Print):2319-672, Vol-3, Issue1, January 2014, pp 39-47.
- [16] **“Approach for the segmentation of satellite images using K-means,KFCM, moving KFCM and Naïve Bays Classifier ”** International Journal of computer applications vol 65(20), March 2013, pp21-26
- [17] **“High Resolution Adaptive Waveform”** International Journal of Advance Electronics and Radar.” Vol -2, Issue 1, pp 21-31.
- [18] **“Polyphase codes Sidelobes Suppression”** IJECE Vol 2, Issue 4, ISSN: 2393-9028 (PRINT) | ISSN: 2348-2281 (ONLINE) , Oct-Dec-2014,pp 47-50.
- [19] **“Implementation of BLI DF Algorithm”** SSRG International Journal of Electronics and Communication Engineering (SSRG-IJECE) – volume1 issue 8, ISSN: 2348 – 8549, Oct 2014, pp 29-35.
- [20] **“VHDL Implementation of Modified Genetic Algorithm”** International Journal of Scientific Engineering and Technology Research (IJSETR), ISSN 2319-8885 Vol.04,Issue.41, October-2015, pp:9026-9028.
- [21] **“Target Detection Using Cross Ambiguity Function”** International Research Journal of Engineering and Technology (IRJET), e-ISSN: 2395 -0056, p-ISSN: 2395-0072,Volume: 02 Issue: 06 Sep-2015, pp1-5.
- [22] **“Comparision of Clustering Algorithms usingNeural Network Classifier for Satellite Image Classification”** *Int. Journal of Engineering Research and Applications* www.ijera.com ISSN : 2248-9622, Vol. 5, Issue 6, ( Part - 5) June 2015, pp.73-80
- [23] **“Simulation of Radar Architecture in MATLAB”** , IJRECE VOL. 5 ISSUE 1 JAN.-MAR. 2017, ISSN: 2393-9028 (PRINT) | ISSN: 2348-2281 (ONLINE), **pp13-18**
- [24] , **“Sidelobe reduction technique for radar pulse compression codes”**, IJRECE VOL. 5 ISSUE 1 JAN.-MAR. 2017, ISSN: 2393-9028 (PRINT) | ISSN: 2348-2281 (ONLINE), pp08-12,
- [25] **“Optimization of half wave dipole array forgeneration of ultra-low side lobes”** International Journal of Engineering Science and Technology (IJEST)ISSN : 0975-5462, Vol. 9 No.08 Aug 2017, pp 853-59
- [26] **“Three Sixty Phase Code Design with Good Discrimination Factor”** International Journal of Electronics, Electrical and Computational System (IJEECS), ISSN 2348-117X , Volume 6, Issue 9, September 2017, pp 331-334.
- [27] **“Study of Optimization Techniques in Generation of Low Side Lobe Patterns”** IOSR Journal of Electronics and Communication Engineering (IOSR-JECE) e-ISSN: 2278-2834,p- ISSN: 2278-8735.Volume 12, Issue 4, Ver. II (Jul.-Aug. 2017), PP 61-68
- [28] **“Design of 8 element micro strip patch array with hybrid power divider for side lobe”** Journal of Advance Research in Dynamical & Control Systems, Vol. 10, 04-Special Issue, 2018, 10 April 2018, ISSN 1943-023X, pp 765-773.
- [29] **“Binary Overlay on Train of Pulses for Good Correlation Properties”** International Journal of Research, Vol. 05, Issue-17, July 2018, **e-ISSN: 2348-6848, p-ISSN: 2348-795X**, pp 426-431

**c) Eight Papers are presented at National level Conferences( S.No 30 -37)**

- [30] **“Radar signal processing for detection of target embedded in clutter”**, IETE Convention, Vishakapatnam, 20 to 21 September 2003.
- [31] **“Radar signal identification using real-time signal processing on DSP-TMS 320 C5X”**, IETE Convention, Vishakapatnam, 20 to 21 September 2003.
- [32] **“Six -phase sequences with good correlation properties”**, Proc of ATC IETE 07, Hyderabad, India , 27-29 September 2007.
- [33] **“Orthogonal Eight -Phase Sequence Sets Design”**, GNSS-2007, Hyderabad, pp 89-93, 25-26 October 2007.

- [34] **“Orthogonal Sixteen-phase Sequence Sets Design for Radar”**, presented in national Conference NCASP-09, 39-43 December 2009, AU,Visakapatnam.
- [35] **“Review of Performances of various coded Sequence Set for MIMO Radar”**, 1<sup>st</sup> National Conference on Recent Advancements in Electronics (NCRAE-16), 22 – 23 January – 2016.
- [36] **“Modelling and simulation of Radar architecture in simulink”** , SIGNAL PROCESSING, EMBEDDED AND COMMUNICATION SYSTEMS(ASPECS-2016), 11-12 August 2016.
- [37] **“Thirty Six Phase Code Design”** , SIGNAL PROCESSING, EMBEDDED AND COMMUNICATION SYSTEMS(ASPECS-2016), 11-12 August 2016.

**d) Forty Eight Papers are presented at International level Conferences (S.No 38-86)**

- [38] **“A direct digital frequency synthesizer using an IIR resonator ”** Proceedings of International Conference, ICSCI-2004, Hyderabad, A,P, 12-15 February, 2004.
- [39] **“extraction of linear and curvilinear segments from satellite images”** Proceedings of International Conference, ICSCI-2004, Hyderabad, A,P, 12-15 February, 2004.
- [40] **“Orthogonal Polyphase Sequence Sets Design for Radar Systems”**, Proc of 7th International Radar Symposium Poland, 24-26 May 2006.
- [41] **“Moving Target detection Using real time signal processing on DSP Processor”** Proc of International conference on Systemics, Cybernetics and Informatics, Vol.1, pp. 5-8, February 2004.
- [42] **“Composite study on Ambiguity Function of Radar signals”** Proc of International conference on Systemics, Cybernetics and Informatics, Vol.1, pp. 563-567, 06-09 January , 2005.
- [43] **“Pulse Train of Multicarrier Complimentary Phase coded signal for favourable autocorrelation and ambiguity function”** Proc of International conference on Systemics, Cybernetics and Informatics, Vol.1, pp. 853-857, 06-09 January, 2005.
- [44] **“A Modified Simulated Annealing algorithm for Binary coded Radar signal Design”** Proc of International Radar Symposium India- 2005, Bangalore, pp 693-697, 19-22 Dec 2005.
- [45] **“Poly Phase code design for Multiple Radar systems”** Proc of International conference on Systemics, Cybernetics and Informatics, Vol.1, pp. 182-187, 04-08 January , 2006.
- [46] **“Modified simulated Annealing Algorithm for Poly Phase Code Design”** Proc of IEEE ISIE - 06, Canada, pp 2966-2971, 22-24 July, 2006.
- [47] **“Polyphase Coded signal Design for Netted Radar Systems ”** Proc of CIE International Conference on Radar, Sanghai , China, October 16-19, 2006,
- [48] **“A Modified simulated Annealing Algorithm for Coded signal Design”** Proc of ANNIE 2006, St Louis, USA, pp 81-86, November 5-8, 2006.
- [49] **“Polyphase Coded sequence sets Design”** Proc of IEEE -ICSIP 2006, Hubli, India pp 891-896, December 7-9, 2006.
- [50] **“Modified Simulated Annealing Algorithm for Binary Coded signal sets Design”**, accepted in IEEE International Conference, TECON-06, Hong - kong
- [51] **“Discrete Frequency Coded sequence sets Design for Radar”**, International conference on Systemics, Cybernetics and Informatics, Hyderabad India pp 849-853, 3-7 January , 2007.
- [52] **“Polyphase sequences with good correlation properties ”** accepted in , IEEE International conference EUROCON -07, to be held in Poland on 9-12 September 2007.
- [53] **“Thirty two-phase coded sequences design for radar ”** Proc of International Radar Symposium India- 2007, Bangalore. pp 12-16, 11-14 December 2007.

- [54] **"Polyphase Sequences Design Using MSA"**, proceeding of IEEE ICCIMA 2007, India ,Vol- IV, pp 357-362, 13-15 December 2007.
- [55] **"Binary Sequences Design with Good Merit Factor Using MSA"** Proc of International conference on Systemics, Cybernetics and Informatics, pp 138-142, 02-05 January , 2008.
- [56] **"Orthogonal Twelve Phase Sequence Sets Design "**, presented in IEEE TENCON 2008, Nov 2008, Hyderabad, India.
- [57] **"Sixty- Phase Code Design Using Modified Genetic Algorithm"**, presented in IEEE Conference Race 08, 20-22 December 2008, Hyderabad.
- [58] **"A new hybrid algorithm for Polyphase code design"**, ANNIE 2007, pp 573-578, , USA, 11-14 November 2007.
- [59] **"Discrete Frequency Coded waveform design"**, in Proc of 14th IEEE Conference ICECS, Morocco, pp 693-696, 11-14 December 2007.
- [60] **"FPGA Implementation of the Ternary Pulse Compression Sequences"** IAENG Conferences - IMECS 2008 held on 19-21 March, 2008 at Hong Kong.
- [61] **"Sixteen Phase Code Design Using Modified Genetic Algorithm"** accepted in Annie 2008 to be held in St Louis, USA.
- [62] **"Orthogonal Eight -phase Sequence Sets Design using MSA"**, WSEAS Conferences, Spain, pp 236-240, September 23-25, 2008.
- [63] **"Thirty -two Phase Sequence sets Design with Good Correlation Properties for Radar"** accepted in Annie 2009 to be held in St Louis, USA.
- [64] **"Discrete Frequency Codes Design accepted in ANNIE 2010 held in St Louis, USA, DEC 2010.**
- [65] **"Performance of Various filters for Salt and Pepper Noise and Speckle Noise"** International Conference on Machine Intelligence application to Power, Signal Processing , Communication and Control( MIPSCON)-2011, PP- SP136-SP139, 7-9 April 2011.
- [66] **"Binary Codes Design with Good Merit Factor"** accepted in IEEE conference, ICACCT-2011.
- [67] **" Orthogonal Sixty -phase Sequence Sets Design using HA"** International Conference, Vol- 1/2, 15-17 Feb, ICSCI-2012, pp-367-370, Hyderabad
- [68] **"Sixty- Four phase Code Design with good Ambiguity Function"** International Conference ICCSA-2012, pp-114-117 Hyderabad
- [69] **"Binary Codes Design with Good Merit Factor"** Proceeding of International conference on Innovation in Electronics and Communication Engineering (ICIECE-2012), pp 109, 20-21 July 2012.
- [70] **"Ninety Phase Code Design with Good Merit Factor final "** International Microwave Symposium ISM-2012, 12-14 December 2012, Bangaluru.
- [71] **"Forty-Eight Phase Code design with good Merit Factor"** IEEE International Conference ICCIC-2012, pp- 320-323 , 18-20 December 2012 , Coimbatore, India
- [72] **"Ninety Phase Code Design with Good Discrimination Factor "** Symposium on Recent Advances in Telecommunication Technology" Tele TECH -13 , 11-12 Febuary 2013, Chennai.
- [73] **"Ninety Six Phase Code Design with Good Discrimination Factor "** 29th Convention of Electronics & Telecommunication Engineers, October 29-30, 2013, Hyderabad, pp 232-238
- [74] **"One Twenty Phase Code design with good Discrimination Factor"** International Radar Symposium India-13 (IRSI-13),11-14 December 2013 , Bangalore, India, pp 38-41.
- [75] **"Segmentation and Classification of satellite Images"** International Conference on Emerging Trends in Instrumentation, Communication, Electrical & Electronics (ICE-ICEE) 23-25 January 2014, pp 27, Indore.



- [76] **“Orthogonal Forty Eight Phase Sequences design using MGA”** Proceeding of International Conference on Innovations in Electronics and Communication Engineering(ICIECE-14), Hyderabad, India-14, pp 48
- [77] **“One Twenty Eight Phase Sequences Design Using MGA”**accepted in **GEM'14** - International Conference on Genetic and Evolutionary Methods, July 21-24, 2014, USA.
- [78] **“Hybrid clusteing algorithm and Neural Networkclassifier for satellite image classification”** International Conference on Industrial Instrumentation and Control (ICIC) 2015,College of Engineering Pune, India. May 28-30, 2015, pp 1379-1383.
- [79] **“Orthogonal Sixty Four Phase Sequences design using MGA”** Proceeding of International Conference on Innovations in Electronics and Communication Engineering(ICIECE-15), Hyderabad, India-15, pp305-308.
- [80] **“Binary Codes Sidelobes Reduction Techniques”** ” **accepted in** International Radar Symposium India-15 (IRSI-15) , 16-17, December 2015 , Bangalore, India.
- [81] **“One Eighty Phase Code Design with Good Discrimination Factor ”** Proceeding of International Conference on Innovations in Electronics and Communication Engineering(ICIECE-16), 8-9 July 2016 , Hyderabad, India, **pp 11**
- [82] **“clustering and Classification of Images Using ABC-KFCM and Neural Network classifier”** International Conference on Communication, Signal Processing, Computing and information Technology(ICCSPCIT-16) 2016, Mall Reddy College of Engineering Hyderabad , ISBN 978-93-83038 48 9, Dec16-17, 2016, pp44-48
- [83] **“Classification of Satellite Images”** Proceedings Of ISERD-IRF International Conference, 25 December 2016, Pune , India, ISBN: 978-93-86291-76-9
- [84] **“Orthogonal Ninety Six Phase sequences Design for MIMO Radar ”** Proceeding of 7<sup>th</sup> IEEE International Conference on Advance Computing Conference (IACC-17), ISBN-978-93-5267-908-9, 5-7 January 2017, Hyderabad, India, pp 12.
- [85] **“Three Sixty Phase Code Design with Good Discrimination Factor”** 4th International Conference on New Frontiers of Engineering, Science, Management and Humanities (ICNFESMH-2017), (IETE, Hyderabad) @ Institution of Electronics and Telecommunication Engineers, O. U Campus, Hyderabad - 500 007, ISBN 978-81-934288-3-2, 3 SEPTMBER 2017, PP 1275-78.
- [86] **“Secured Home Automation Using Internet of Things”** Proceeding of 33<sup>rd</sup> Convention of Electronics and telecommunication engineering, IE(I), November 29-30, 2017, pp 44-49.