



# Mr.K.N.Mallesh

## Assistant Professor



Education Qualifications: Master Of Technology ( ECE)  
Specialization: VLSI System Design

### ADDRESS:

- C- Block -Room No 305

### JNTUH ID:

2238-150620-121536

### EMAIL:

knmallesh\_ece@mgit.ac.in

### DATE OF JOINING:

08-06-2015

### EXPERIENCE - 7 Years

- Teaching - 7
- Research -0
- Industry -0
- Others –

### SUMMARY:

- Publications -
- Conferences -
- Patents –
- Books –
- Honors/Awards -

### EVENTS:

- Organized -
- Attended - 7

### Responsibilities Held at Institution Level:

1. Anti-ragging duties.
2. Campus monitoring duties

### Responsibilities Held at Department Level:

1. Maintains Faculty Interaction with outside world-NBA file.
2. Class In charge

### Courses Handled at Under Graduate /Post Graduate Level:

- **UG:** Linear IC applications,
- Analog electronics
- Digital electronics
- Network theory
- Analog and digital circuits

### Refresher Courses/ Workshops/ Webinars/ Seminars/ Guest Lecture:

1. One day webinar on “Clock Domain Crossing Techniques & Clock Gating Design(VLSI)” 2020
2. Attended webinar on "Applications of Satellite Images & Remote Sensing Data” 2020.
3. Attended webinar on “Analog VLSI design” 2020
4. Attended Webinar on “outcome based education” 2020

### Events Attended

#### FDPs/STTPs:

1. Attended 2 week FDP on “Recent Trends in IOT Using Embedded systems on 1-13 February 2021 in online mode conducted by ECE department MGIT
2. Attended 1 week FDP on “Future Nano electronic devices and circuits on 6-10 July 2020 in online mode conducted by ECE department MGIT
3. Attended 2 days FDP on “Effective Technical Writing Using LATEX on 8-9 June 2020 in online mode conducted by ECE department MGIT

### Online Certifications:

1. Completed Introduction To Electronics subject in coursera platform in May 2020
2. Completed one NPTEL course on “Control Systems Engineering” in November 2019