Mr. M. Naga Sitaram
Assistant Professor

Education Qualifications: Master of Technology (ECE Department)
Specialization: Digital Systems and Computer Electronics

ADDRESS:
- C – Block -Room No 404

JNTUH ID:
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DATE OF JOINING:
05-01-2017

EXPERIENCE - 14 Years
- Teaching - 7
- Research - 2
- Industry - 5

SUMMARY:
- Publications - 1
- Conferences - 1
- Books – 1
- Honors/Awards - 5

EVENTS:
- Organized - 2
- Attended - 12

LET'S MEET ON SOCIAL:
- https://www.quora.com/profile/Naga-Sitaram-M
- https://in.linkedin.com/in/naga-sitaram-m-39110b22
- https://twitter.com/amsitaram
- https://www.facebook.com/amsitaram

Membership of Professional Bodies:

1. IETE

Responsibilities Held at Institution Level:

1. PRERANA (scheme by AICTE) ECE Faculty In-charge
2. Team Member of Nirvana College Fest Organizing Committee
3. Anti-ragging duties and Student Monitoring

Responsibilities Held at Department Level:

1. Lab In-charge from M.Tech Wireless Communication and Networks Lab
2. Part of Extra-Curricular Activities Committee for Competitive Exam training (GATE) for students
3. Member of committee for organizing Microcosm Technical fest

Honors/Awards Received:
1. Secured 1st Rank in APRCET -2019 for Ph.D. (Part-Time) Admission in Andhra University.
2. Qualified National Eligibility Test for Assistant Professor for the subject Electronic Science in 2018.
3. Qualified in Faculty Eligibility Test 2010 by JNT University, Hyderabad.
4. GATE scholarship by MHRD awarded in 2006-2008 during M.Tech in IIIT, Hyderabad
5. Certified as EXCELLENT in "Honors diploma in Web Applications", from NIIT, in March 2006.

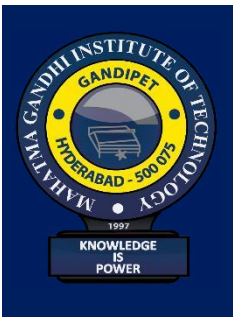## Courses Handled at Under Graduate /Post Graduate Level:

- **UG:** Digital Design using Verilog, Embedded System Design, Intellectual Property Rights, Computer Organization and Operating Systems, Fundamentals of Internet of Things, Digital Logic Design, Principles of Electronic Communication, Principles of Computer Communication and Networks

- **PG:** DSP and Processors, Digital Logic Design lab, Wireless Communications and Networks lab, Research Methodology & IPR

Publications:

1. Published a paper "Novel High-Speed Vedic Mathematics Multiplier Using Reversible Logic" in International Journal Of Global Innovations-Vol.2, Issue .I (IJGIS -2014) Paper Id: SP-V2-I1-255, ISSN Online: 2319-9245

Conferences:

1. Runner-up in TechnoZion 2006, a National Level Technical Conference at NIT Warangal for paper "ADC to Reduce Power Consumption in Modern VLSI ICs."

No. of Books/Chapter Published with details:

1. As an Author Contributed Unit 1, Unit 4, Unit 7 in Part 3 of Trishna Knowledge Systems - GATE Electronics and Communication Engineering: Pearson Education.

Events Organized:

FDPs/STTPs:

1. SIMULATION AND MODELING OF DATA COMMUNICATIONS & NETWORKS, by Dept. of ECE, MGIT in association with TASK, from 27th to 31st July , 2020.

Refresher Courses/ Workshops/ Webinars/ Seminars/ Guest Lecture:

1. Coordinator for ILLUMINATI 2010, a National Level Techno-Management Fest, at DRK CET, Hyderabad.

Events Attended

FDPs/STTPs:

1. ATAL FDP on "Architecture and Technologies for 5G and Beyond Wireless Networks" from 16/08/2021 to 20/08/2021 at the Indian Institute of Technology (BHU) Varanasi.

2. Universal Human Values FDP -Inculcating Universal Human Values in Technical Education Organized by AICTE from 14 June to 18 June 2021

3. ATAL FDP on "Internet of Things" from 01/06/2021 to 05/06/2021 at National Institute of Technology Hamirpur Himachal Pradesh.

4. ATAL FDP on "Global Navigation Satellite System (GNSS)" from 2020-12-18 to 2020-12-22 at National Institute of Technology Mizoram.

5. ATAL FDP on "Internet of Things (IoT)" from 2020-9-21 to 2020-9-25 at NATIONAL INSTITUTE OF ELECTRONICS AND INFORMATION TECHNOLOGY (NIELIT)- GANGTOK.

6. Secured A+ grade in Faculty Development Program organized by IIT Roorkee for the summer course "Digital VLSI Circuit Design" in June 2017.

Refresher Courses/ Workshops/ Webinars/ Seminars/Guest Lecture:

1. Refresher Programme on Opportunity, Challenges and Research Trends in Wireless Communication from 23-29th April 2021 by GNIT, Hyderabad approved by AICTE & ISTE

2. Online Training Programme on Digital Tools for Writing Authoring and Reviewing Manuscripts during 21-09-2020 to 02-10-2020 organized jointly by MNIT Jaipur, NIT Patna, & IIT Guwahati

3. National Education Policy 2020- Higher Education Perspectives webinar in Sept 2020 by National Institute of Technical Teacher Training and Research, Bhopal

4. Accreditation Webinar Series in July -Aug 2020, by National Institute of Technical Teacher Training and Research, Bhopal

5. Trained on "System Design using FPGA" at C-DAC, Hyderabad in June 2009.

6. Participated in 21st International Conference on VLSI Design 2008, and 8th International Conference on Embedded Systems 2008, Hyderabad

Online Certifications:

1. Microelectronics: Devices to Circuits from NPTEL

Any Other Contribution:

1. Qualified in GATE (Electronics and Communications) from 2006 to 2018.

2. Trained the Aspirants for GATE / Engineering Services / other Competitive Exams / Campus Placements.