## B.Tech. III Semester End Examinations

(Model Question Paper)
Course Title: Probability Distributions and Numerical Methods
Course Code: MA303BS
Max. Marks: 70
Time: 3 hours
Note: Answer ALL Questions
Part-A (10 x 2 = 20 Marks)

| Q. No. | Stem of the Question |  |  |  |  |  | M | L | CO | PO |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  | Unit-I |  |  |  |  |
| 1. a) | What is the probability that a card drawn at random from the pack of playing cards may be either a queen or a king |  |  |  |  |  | 2 | 1 | 1 | 1 |
| 1. b) | Calculate expectation of X , if the probability distribution of the random variable X is given by |  |  |  |  |  | 2 | 3 | 1 | 2 |
|  | x | -1 |  | 1 |  | 3 |  |  |  |  |
|  | f | 0.3 | 0.1 | 0.1 | 0.3 | 0.2 |  |  |  |  |
| Unit-II |  |  |  |  |  |  |  |  |  |  |
| 1. c) | Solve for the values of $\mathrm{n} \& \mathrm{p}$ of the binomial distribution for which the mean is 4 and variance is 3 |  |  |  |  |  | 2 | 3 | 2 | 1 |
| 1.d) | If a random variable has a poisson distribution such that $\mathrm{P}(\mathrm{x}=1)=\mathrm{P}(\mathrm{x}=2)$ find the Mean of the Poisson distribution. |  |  |  |  |  | 2 | 1 | 2 | 2 |
| Unit-III |  |  |  |  |  |  |  |  |  |  |
| 1.e) | Explain type I error |  |  |  |  |  | 2 | 5 | 3 | 2 |
| 1.f) | write the test statistic for large sample single mean |  |  |  |  |  | 2 | 1 | 3 | 2 |
| Unit-IV |  |  |  |  |  |  |  |  |  |  |
| 1. g) | Mention the first approximation of one of the roots $f(x)=0$ by regula falsi method under the conditions that $\mathrm{f}(\mathrm{a}) \& \mathrm{f}(\mathrm{b})$ have opposite signs and $\mathrm{a}<\mathrm{b}$ |  |  |  |  |  | 2 | 1 | 4 | 1 |
| 1.h) | what is the condition for the convergence of successive approximation method |  |  |  |  |  | 2 | 1 | 4 |  |
| Unit-V |  |  |  |  |  |  |  |  |  |  |
| 1. i) | $\mathrm{f}(\mathrm{x})$ is given by |  |  |  |  |  |  |  |  |  |
|  | x |  |  | 1 |  |  |  |  |  |  |
|  | $\mathrm{f}(\mathrm{x})$ | 1 | 0.8 | 0.5 |  |  | 2 | 3 | 5 | 1 |
|  | Then use Trapizoidal rule to calculate $\int_{0}^{1} f(x) d x$ |  |  |  |  |  |  |  |  |  |
| 1.j) | What is the Simpsons $3 / 8$ rule to evaluate $\int_{a}^{b} f(x) d x$ |  |  |  |  |  | 2 | 1 | 5 | 1 |

Part-B ( $5 \times 10=50$ Marks)

| Q. No. | Stem of the Question | M | L | CO | PO |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Unit-I |  |  |  |  |  |
| 2.a) | Of the 3 men the chances that a politician a businessman or an academician will be appointed as a vice chancellor of a university are $0.5,0.3,0.2$ respectively. Probability that research is promoted by these persons if they are appointed as vice chancellor are $0.3,0.7,0.8$ respectively. If research is promoted, what is the probability that VC is an academician. | 5 | 1 | 1 | 2 |
| 2.b) | Two aeroplanes bomb a target in succession. The probability of each correctly scoring a hit is 0.3 and 0.2 respectively. The second will bomb only if the first misses the target. Find the probability that <br> i) target is hit <br> ii) both fail to score hit | 5 | 1 | 1 | 2 |
|  | OR |  |  |  |  |
| 2. c) | A player wins if he gets 5 on a single throw of a die, he loses if he gets 2 or 4 . If he wins, he gets ₹ 50 , if he loses he gets ₹ 10 , otherwise he has to pay ₹ 15 . Find the value of the game to the player. Is the game favourable to the player? | 5 | 1 | 1 | 2 |
| 2.d) | If a random variable has the probability density function $\begin{array}{rlrl} f(x) & =\mathrm{k}\left(x^{2}-1\right), & -1 \leq \mathrm{x} \leq 3 . \\ & =0 & & \text { elsewhere } \end{array}$ | 5 | 1 | 1 | 2 |



M: Marks; L: Bloom’s Taxonomy Level; CO: Course Outcome; PO: Programme Outcome

## B.Tech. III Semester End Examinations <br> (Model Question Paper)

Course Title: Analog and Digital Electronics
Time: 3 hours

> Note: Answer ALL Questions
> Part-A $(10 \times 2=20$ Marks $)$

| $\begin{gathered} \hline \mathbf{Q} . \\ \text { No. } \end{gathered}$ | Stem of the Question | M | L | CO | PO |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Unit-I |  |  |  |  |  |
| 1. a) | What is meant by Zener Breakdown? | 2 | 2 | 1 | 1 |
| 1. b) | What is diffusion capacitance? | 2 | 1 | 1 | 1 |
| Unit-II |  |  |  |  |  |
| 1. c) | Write any two differences between CB and CE configurations? | 2 | 1 | 2 | 1 |
| 1. d) | What is the need of transistor biasing? | 2 | 2 | 2 | 1 |
| Unit-III |  |  |  |  |  |
| 1.e) | Write any two differences between BJT and JFET? | 2 | 2 | 3 | 1 |
| 1.f) | Draw the diagram of CMOS NOT gate? | 2 | 1 | 5 | 2 |
| Unit-IV |  |  |  |  |  |
| 1.g) | Write the logic of half adder with Boolean functions? | 2 | 1 | 4 | 2 |
| 1. h) | Draw the $4 \times 1$ multiplexer? | 2 | 1 | 4 | 1 |
| Unit-V |  |  |  |  |  |
| 1. i) | Define T Flip Flop with the help of characteristic equation? | 2 | 1 | 4 | 1 |
| 1. j) | Differentiate Mealy Machine and Moore Machine? | , | 2 | 4 | 2 |

Part-B (5 x 10=50 Marks)

| Q. No. | Stem of the Question | M | L | CO | PO |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Unit-I |  |  |  |  |  |
| 2. a) | Explain the operation of PN Junction Diode with the help of VI characteristics? | 5 | 2 | 1 | 1 |
| 2. b) | Draw the diagram of bridge rectifier and derive the efficiency and ripple factor? | 5 | 3 | 1 | 2 |
| OR |  |  |  |  |  |
| 2. c) | Explain the operation of Photo Diode with the help of VI characteristics? | 5 | 2 | 1 | 1 |
| 2. d) | Compare half wave and full wave rectifiers? | 5 | 3 | 1 | 2 |
| Unit-II |  |  |  |  |  |
| 3. a) | Draw CB configuration of transistor and explain its input and output characteristics? | 5 | 1 | 2 | 1 |
| 3.b) | Calculate operating point for the fixed bias circuit? | 5 | 3 | 2 | 2 |
| OR |  |  |  |  |  |
| 3.c) | Draw CE configuration of transistor and explain its input and output characteristics? | 5 | 2 | 2 | 1 |
| 3. d) | Solve the expression for stability factor in voltage divider bias circuit? | 5 | 3 | 2 | 2 |
| Unit-III |  |  |  |  |  |
| 4. a) | Explain the JFET operation with the help of diagrams? | 5 | 2 | 1 | 1 |
| 4. b) | Draw CMOS NOR gate and explain with neat diagram. | 5 | 3 | 5 | 2 |
| OR |  |  |  |  |  |
| 4.c) | Draw the Drain and Transfer characteristics of Depletion MOSFET and explain with neat diagrams? | 5 | 2 | 1 | 1 |
| 4. d) | Realize XOR gate using NAND gates? | 5 | 2 | 5 | 3 |

## Unit-IV

| Unit-IV |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 5. a) | Derive the logic expression for full adder with the help of truth table? | 5 | 3 | 4 | 2 |
| 5. b) | Obtain minimal POS expression for the Boolean function. $\mathrm{F}(\mathrm{A}, \mathrm{B}, \mathrm{C}, \mathrm{D})=\Pi(0,1,2,3,4,6,9,10)+\mathrm{d}(7,11,13,15)$.draw the circuit using 2 input NAND gates | 5 | 1 | 3 | 2 |
| OR |  |  |  |  |  |
| 5.c) | Design 2 Bit Comparator and draw logic diagram? | 5 | 3 | 4 | 3 |
| 5. d) | Explain the differences between canonical SoP and canonical PoS with suitable example | 5 | 2 | 3 | 2 |
| Unit-V |  |  |  |  |  |
| 6. a) | Draw the diagram of RS flip flop and explain the operation with truth table | 4 | 2 | 4 | 2 |
| 6. b) | Design and develop Mod-10 Asynchronous counter using T- FlipFlop | 6 | 6 | 4 | 3 |
| OR |  |  |  |  |  |
| 6. c) | Design Mod 6 synchronous counter? | 6 | 6 | 4 | 3 |
| 6.d) | Explain PISO shift register with the help of neat diagram | 4 | 2 | 4 | 1 |

M: Marks; L: Bloom's Taxonomy Level; CO: Course Outcome; PO: Programme Outcome

## B.Tech. III Semester End Examinations <br> (Model Question Paper)

Course Title: Data Structures
Time: 3 hours

Course Code: CS301PC
Max. Marks: 70

## Note: Answer ALL Questions <br> Part-A (10 x $2=20$ Marks)

| Q. No. | Stem of the Question | M | L | CO | PO |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Unit-I |  |  |  |  |  |
| 1. a) | Define a Data Structure. What are the different types of Data Structures? | 2 | 1 | 1 | 1,2 |
| 1.b) | Define ADT. Write the operations on data structures. | 2 | 1 | 1 | 1,2 |
| Unit-II |  |  |  |  |  |
| 1. c) | Differentiate between Single Linked list and Doubly Linked list | 2 | 2 | 2 | 1,2 |
| 1.d) | Write the node structure in a Circular Linked list | 2 | 2 | 2 | 1,2 |
| Unit-III |  |  |  |  |  |
| 1. e) | What is a Binary tree? Mention the tree traversals | 2 | 1 | 3 | 1,2 |
| 1.f) | Write the properties of Binary Search Tree. | 2 | 2 | 3 | 1,2 |
| Unit-IV |  |  |  |  |  |
| 1.g) | Define a Graph. What are different Graph traversals? | 2 | 1 | 4 | 1,2 |
| 1.h) | Differentiate between Linear Search and Binary Search. | 2 | 2 | 4 | 1,2 |
| Unit-V |  |  |  |  |  |
| 1. i) | Give example of Folding Hash method | 2 | 2 | 5 | 1,2 |
| 1.j) | What are the different collision resolution techniques? | 2 | 1 | 5 | 1,2 |

Part-B (5 x 10=50 Marks)

| Q. No. | Stem of the Question | M | L | CO | PO |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Unit-I |  |  |  |  |  |
| 2.a) | Explain evaluation of postfix expression with an example | 4 | 2 | 1 | 1,2 |
| 2.b) | Write a C program for Queues using arrays. | 6 | 3 | 1 | $\begin{gathered} 3, \\ 12 \end{gathered}$ |
| OR |  |  |  |  |  |
| 2.c) | What is stack? What are the basic operations associated with stack? | 5 | 1 | 1 | 1,2 |
| 2.d) | Convert following arithmetic infix expression into postfix by using stack : $\mathrm{A} *(\mathrm{~b}+\mathrm{c})+(\mathrm{b} / \mathrm{d}) * \mathrm{a}+\mathrm{z} * \mathrm{u}$ | 5 | 2 | 1 | $\begin{aligned} & 3, \\ & 12 \end{aligned}$ |
| Unit-II |  |  |  |  |  |
| 3.a) | Write the algorithm for insertion of a new node as last node in Doubly linked list. | 4 | 2 | 2 | 3 |
| 3.b) | Implement insert() and delete() functions in Queues using Single linked list | 6 | 3 | 2 | $\begin{gathered} 3, \\ 12 \end{gathered}$ |
| OR |  |  |  |  |  |
| 3.c) | Implement push() and pop() functions in Stacks using Single linked list | 6 | 3 | 2 | $\begin{gathered} 3, \\ 12 \end{gathered}$ |
| 3.d) | Write the algorithm for insertion of a new node as middle node in Single linked list | 4 | 2 | 2 | 3 |
| Unit-III |  |  |  |  |  |
| 4.a) | Construct a Binary Search tree using the elements 14,25,51,12,21,45,11,10 | 4 | 4 | 3 | 1,2 |
| 4.b) | Construct a binary tree having the following traversal sequences: Preorder traversal: A B C D E F G H I Inorder traversal: B C A E D G H F I | 6 | 4 | 3 | 1,2 |
| OR |  |  |  |  |  |
| 4.c) | Define a Binary Tree. Explain the Binary tree representations with an example | 5 | 1 | 3 | 1,2 |


|  | Write a brief note on Traversing a binary tree. Find the preorder and <br> postorder traversal of following tree. <br> Root |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- |

M: Marks; L: Bloom's Taxonomy Level; CO: Course Outcome; PO: Programme Outcome
(Autonomous)
MR-21

# B.Tech. III Semester End Examinations <br> (Model Question Paper) 

Course Title: Computer Organization and Microprocessor
Time: 3 hours

## Note: Answer ALL Questions

Part-A (10 x $2=20$ Marks)

Course Code: IT301PC
Max. Marks : 70
em of the Question

| $\mathbf{M}$ | L | CO | $\mathbf{P O}$ |
| :--- | :--- | :--- | :--- |


| Q. No. | Stem of the Question | M | L | CO | PO |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Unit - I |  |  |  |  |  |
| 1. a) | Define computer organization, computer architecture. | 2 | 1 | 1 | 1 |
| 1.b) | What is the difference between a direct and an indirect address instruction? | 2 | 4 | 2 | 1 |
| Unit - II |  |  |  |  |  |
| 1. c) | Define 8086 minimum and maximum mode. | 2 | 1 | 4 | 1 |
| 1. d) | If the segment address is 1005 H and the offset address is 5555 H then what is the physical address? | 2 | 3 | 4 | 2 |

Unit - III

| Unit - III |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1. e) | What is an interrupt service routine in microprocessor? | 2 | 2 | 3 | 4 |
| 1.f) | What is the difference between a macro and a procedure? | 2 | 4 | 5 | 1 |
| Unit - IV |  |  |  |  |  |
| 1.g) | Provide the hardware for signed-2's complement addition and subtraction. | 2 | 3 | 1 | 2 |
| 1. h) | Differentiate isolated I/O and memory mapped I/O. | 2 | 2 | 1 | 1 |
| Unit - V |  |  |  |  |  |
| 1. i) | Explain the locality of reference. | 2 | 2 | 2 | 1 |
| 1. j) | Draw the system bus structure for multiprocessors. | 2 | 2 | 6 | 1 |


| Part-B ( $5 \times 10=50 \mathrm{Marks}$ ) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Q. No. | Stem of the Question | M | L | CO | PO |
| Unit - I |  |  |  |  |  |
| 2. a) | List the registers for the basic computer and give their functionality in program execution. | 5 | 2 | 1 | 1 |
| 2. b) | Describe the micro programmed control organization and compare its advantages over hardwired control. | 5 | 4 | 1 | 1 |


| OR |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 2. c) | Elucidate common bus system. | 5 | 4 | 1 | 1 |
| 2. d) | Formulate a mapping procedure that provides eight consecutive micro instructions for each routine. The operation code has 7 bits and control memory has 4096 words. | 5 | 6 | 2 | 2 |


| Unit - II |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 3. a) | Explain the register organization in 8086. | 5 | 2 | 4 | 1 |
| 3.b) | Discuss the Physical memory organization. | 5 | 2 | 2 | 1 |
| OR |  |  |  |  |  |
| 3. c) | Does 8086 support instruction pipelining? Justify your answer with relevant example instructions. | 5 | 4 | 3 | 1,2 |
| 3. d) | Discuss the physical address formation in different addressing modes. | 5 | 3 | 4 | 1,2 |
| Unit - III |  |  |  |  |  |
| 4. a) | Develop an assembly language program to find out numbers odd and even numbers in a given series of 16 -bit hexa decimal numbers. | 5 | 3 | 5 | 2 |
| 4. b) | What do you mean by a macro? What are the differences between a macro and a subroutine? | 5 | 4 | 5 | 1,2,3 |
| OR |  |  |  |  |  |
| 4. c) | Write a program to find out the number of positive numbers and negative numbers from a given series of signed numbers. | 5 | 3 | 5 | 2 |
| 4. d) | Elaborate on the techniques used to pass parameters to procedures in assembly language program. | 5 | 3 | 4 | 1,2 |


| Unit - IV |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 5. a) | Explain Booths multiplication algorithm with example. | 5 | 3 | 3 | 1,2 |
| 5. b) | Compare interrupt driven data transfer scheme with DMA. Using block diagram explain interrupt driven transfer scheme. | 5 | 4 | 1 | 1,3,4 |
| OR |  |  |  |  |  |
| 5. c) | Explain arithmetic pipeline with example. | 5 | 2 | 3 | 3,4 |
| 5. d) | Illustrate asynchronous communication interface in detail. | 5 | 2 | 3 | 1 |


| Unit - V |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 6. a) | A digital computer has a memory unit of $64 \mathrm{~K} * 16$ and a cache memory of 1 K words. The cache uses direct mapping with a block size of 4 words. <br> i. How many bits are there in the tag, index, block and word fields of the address format? <br> ii. How many bits are there in each word of cache and how are they divided into function? Include a valid bit. | 5 | 5 | 2 | 1,2,3 |
| 6. b) | Explain various Interconnection Structures. | 5 | 4 | 6 | 1,3 |
| OR |  |  |  |  |  |
| 6. c) | Give a neat sketch that illustrates the components in a typical memory hierarchy. | 5 | 4 | 2 | 1,3 |
| 6. d) | Elucidate array processor in detail. | 5 | 2 | 6 | 1,3 |

[^0]CO: Course Outcome;
PO: Programme Outcome

# (Autonomous) 

MR-21

## B.Tech. III Semester End Examinations (Model Question Paper)

Course Title: C++ Programming
Time: 3 hours

| Time: 3 hours |  | Max. Marks : 70 |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Note: Answer ALL Questions Part-A (10 x $2=20 \mathrm{Marks}$ ) |  |  |  |  |  |
| Q. No. | Stem of the Question | M | L | CO | PO |
| Unit-I |  |  |  |  |  |
| 1. a) | Differentiate between OOP and procedure oriented programming. | 2 | 4 | 5 | 1,2 |
| 1.b) | Explain structure of C++ program. With example | 2 | 2 | 5 | 1 |
| Unit-II |  |  |  |  |  |
| 1. c) | Illustrate class and object with suitable example. | 2 | 3 | 5 | 1,3 |
| 1. d) | Define the following i) constructor, ii) parameterised constructor. | 2 | 1 | 5 | 1 |
| Unit-III |  |  |  |  |  |
| 1. e) | How reusability is implemented in oop. | 2 | 2 | 1 | 1 |
| 1.f) | What is virtual base class. | 2 | 1 | 1 | 1 |
| Unit-IV |  |  |  |  |  |
| 1.g) | Define file stream. | 2 | 1 | 3 | 1 |
| 1.h) | What is operator over loading | 2 | 1 | 5 | 1 |
| Unit-V |  |  |  |  |  |
| 1. i) | Define function template | 2 | 1 | 4 |  |
| 1. j) | What is the importance of try, catch and throw keywords. | 2 | 1 | 4 | 1 |

Note: Answer ALL Questions
Part-A (10x2 20 Marks )

Part-B (5 x 10=50 Marks)

| Q. No. | Stem of the Question | M | L | CO | PO |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Unit-I |  |  |  |  |  |
| 2. a) | Explain jumping control structures in $\mathrm{C}++$. | 5 | 2 | 5 | 1 |
| 2. b) | Write a C++ program to add corresponding elements of two arrays and store them in a new array using pointers | 5 | 3 | 5 | 1,2 |
| OR |  |  |  |  |  |
| 2. c) | Explain operator precedence with example. | 5 | 2 | 5 | 1 |
| 2. d) | With a suitable example explain NEW and DELETE operators | 5 | 2 | 5 | 1 |
| Unit-II |  |  |  |  |  |
| 3. a) | Explain friend class with suitable example program | 5 | 2 | 5 | 1 |
| 3.b) | Differentiate constructors and destructors. | 5 | 4 | 5 | 1 |
| OR |  |  |  |  |  |
| 3.c) | What is the use of this pointer explain. | 5 | 1 | 5 | 1 |
| 3. d) | Explain different types of constructors with example program. | 5 | 2 | 5 | 1,3 |
| Unit-III |  |  |  |  |  |
| 4. a) | Explain different forms of inheritance .give an example of each. | 5 | 2 | 1 | 1,3 |
| 4. b) | Write a C++ program to demonstrate how access modifier of super class method can be changed in privately derived class | 5 | 3 | 1 | 1,2,3 |
| OR |  |  |  |  |  |
| 4.c) | Differentiate between early binding and late binding. | 5 | 4 | 5 | 1,2 |
| 4. d) | Discuss about virtual function with example program. | 5 | 1 | 1 | 1 |
| Unit-IV |  |  |  |  |  |
| 5. a) | Write a C++ program to create a file of strings and to read the contents of a file line by line. | 5 | 3 | 3 | 1,2,3 |
| 5. b) | Illustrate the use of perror() | 5 | 2 | 3 | 1,3 |
| OR |  |  |  |  |  |
| 5.c) | Write a C++ program to illustrate the use of ferror() | 5 | 4 | 3 | 1 |
| 5. d) | With example explain operator over loading. | 5 | 3 | 2 | 1 |
| Unit-V |  |  |  |  |  |
| 6. a) | Write a C++ Program for exception handling using multiple catch statements | 5 | 4 | 2 | 1,2 |
| 6. b) | Write a program for Bubble Sort using Template Functions. | 5 | 3 | 5 | 1,2 |
| OR |  |  |  |  |  |
| 6.c) | How STL is different from C++ standard library? Explain briefly the three foundational items of standard template library. | 5 | 3 | 2 | 1 |
| 6. d) | Write a C++ program to add two integers, two floating point numbers and two complex numbers using class templates. | 5 | 4 | 4 | 1,2 |

[^1]
[^0]:    M: Marks; L: Bloom's Taxonomy Level;

[^1]:    M: Marks; L: Bloom's Taxonomy Level; CO: Course Outcome; PO: Programme Outcome

