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Vice-Principal

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Mrs. P. Veera Bhadra Kumari

Assistant Professor, EEE

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COORDINATION & TECHNICAL TEAM:

Dr. P. Chandra Sekhar, Associate Professor

Dr. P. Nagasekhar Reddy, Associate Professor

Mr. B. Narsimha Reddy, Assistant Professor

Mr. P. Pradyumna, Assistant Professor

Dr. M. Nalini Devi, Assistant Professor

Dr. A. Ramchandra Reddy, Assistant Professor

Mr. G. Gopal, Assistant Professor

Mr. S. Abhishek Reddy, Assistant Professor

Mrs. S. Sudha Rani, Assistant Professor

Mr. P. Ranjith Krishna, Assistant Professor

Mr. H. Gurunath, Assistant Professor

RESOURCE PERSONS:

1. **Dr. B.Koti Reddy**

Scientific Officer, Dept. of Atomic Energy, Ashwapuram

2. **Dr.D.Sreenivasa Rao**

Associate Professor ,EED, NIT, Warangal

3. **Mr.G.Mahesh Kumar**

DE, TSSPDCL, Hyderabad

4. **Mr.T.Sreedhar**

ADE, TSSPDCL, Hyderabad

5. **Mr.A.Raj Kumar**

Chief Manager, SRLDC, Bangalore

TOPICS

- Digitalization in Electrical Engineering
- Decision support systems in grid control centres- Indian Grid perspective
- De-regulation and market dynamics in Electrical Engineering
- De-carbonization strategies in Electrical Engineering
- Implementation and action planning for integration of 3D's

Department

of

Electrical and Electronics Engineering

is

Organizing

A One Week National Level Online

Faculty Development Programme

on

“Aiming towards 3D’s in Electrical Engineering ”

from

11th to 15th March 2024

Time: 10:30 AM to 12:00 PM

ABOUT FDP:

The Faculty Development Program aimed for Faculty/Research Scholars to empower electrical engineering educators for navigating the 3D's i.e Digitalization, De-carbonization and De-regulation in Electrical Engineering towards sustainable innovation. The program would enable the participants to explore the knowledge on the application of digital technologies in integrating renewable Energy sources with conventional sources to promote de-carbonization and digital transformation of the power sector.

ABOUT THE INSTITUTE:

Mahatma Gandhi Institute of Technology is one of the premier Engineering Colleges in the self – financing category in the state of Telangana. It has grown rapidly since its inception in 1997. The lush green campus of MGIT instituted by Chaitanya Bharthi Educational Society is spread over 30 acres of pleasant landscape with a constructed area of 2,50,787 sq.ft. MGIT is affiliated to Jawaharlal Nehru Technological University, Hyderabad. The institute is accredited by NAAC with 'A++' grade for a period of 5 years and Eight Under Graduate programs of the institute are accredited by National Board of Accreditation, New Delhi. The college offers instruction in 11 undergraduate programs (Electrical and Electronics Engineering, Computer Science Engineering, Computer Science Engineering (Data Science), Computer Science Engineering (Artificial Intelligence and Machine Learning), Computer Science and Business System, Information Technology, Electronics and Communication Engineering, Mechanical (Mechatronics) Engineering, Metallurgical and Materials Engineering, Civil Engineering and Mechanical Engineering) and also 5 PG programs. The college celebrates its annual cultural festival 'NIRVANA' and each of the departments conducts annual technical symposiums & conferences. The Management extends unstinted support in organizing all curricular, co-curricular and extra-curricular activities. In its two decades and more of its evolution, all the stake holders of the Institute, relentlessly endeavored to position MGIT as a Leader and an Innovator in the ecosystem of technical education. The Institute has established excellent Infrastructure such as state-of-the art laboratories, spacious library with a collection of printed and digital books & journals, sports & hostel facilities along with Infrastructure for extra and co-curricular engagements, in pursuit of academic excellence. The institute has received MHRD's NIRF-2022 Ranking in the rank band: 251-300. Starting from academic year 2021-2022 MGIT is conferred with Autonomous status for 10 years by UGC, New Delhi.

ABOUT EEE DEPARTMENT:

The Electrical and Electronics Engineering department was established in the year 2001-2002. The department imparts knowledge in the field of design of electrical equipment. The experienced faculty with the aid of 7 well equipped laboratories impart valuable theoretical & practical knowledge to the prospective engineers to make them future ready. The main objective of the department is to create interest and imbibe fundamental concepts of Electrical Engineering among the students to bridge the gap between academics and industrial needs. Students are always an asset to the institute and nurturing them is of foremost importance. In addition to this, the department also has the Memorandum of Understanding (MoU) with Central Power Training Institute (CPTI), Covenant Technology Solutions, Ademtek Solutions Pvt Ltd, SkillDzire and Hyderabad Institute of Electrical Engineers (HIEE) to have a result- oriented training for Electrical Engineering and Electrical Design Courses. These MoUs provide high-quality education; professional training and continuous guidance to the students helping and guiding them to be placed in core electrical engineering jobs. The dedicated staff members possessing sound knowledge in emerging areas like Power Systems, Power Electronics, Micro Electronics, Control Engineering, etc., motivate and stimulate challenging research projects coupled with high standard of lecture courses, ensuring that the students choose appropriate career path.

ELIGIBILITY:

Faculty / Research scholars from AICTE approved Engineering colleges, Professionals from Industry, Government and R&D organizations are eligible.

FREE Registration

***E-certificate** will be provided to all the active registered participants after successful submission of feedback cum attendance form for all the sessions.

FDP GUIDELINES:

1. Faculty development program platform: Cisco Webex
2. After submitting the Google form for participant registration for the FDP, Whatsapp Link will be provided. All the registered participants for the FDP must join the Whatsapp group for receiving FDP meeting links and any other information regarding FDP.
3. Attending all the sessions and submission of feedback cum attendance form by participants is mandatory.
4. Feedback cum attendance link will be shared in the chat box at the end of the session.
5. E-Certificate will be provided to all the registered participants who attend all the sessions and submit day wise feedback cum attendance forms at the end of each session.
6. The last date for registration is on or before 10th March 2024, 8:00 PM.

How to Register for FDP:

All the Interested persons are informed to fill the Google form through the link given below

<https://forms.gle/UJcb7GxfdRuuYW8AA>

OR

Scan the QR code below to register

