

Dr.A.Navaneetha

Assistant Professor



Education Qualifications: P.hD
Specialization: ECE

ADDRESS:

- C – Block -Room No C-306

JNTUH ID:

93150404-145542

EMAIL:

anavaneetha_ece@mgit.ac.in

DATE OF JOINING:

10-10-2009

EXPERIENCE - 15 Years

- Teaching - 17
- Research - 3
- Industry -
- Others –

SUMMARY:

- Publications - 12
- Conferences - 2
- Patents –
- Books –
- Honors/Awards -

EVENTS:

- Organized - 02
- Attended - 18

LET'S MEET ON SOCIAL:

- <https://www.facebook.com/mgithyderabad>
- <https://www.instagram.com/mgithyderabad>
- <https://www.linkedin.com/company/mgithyderabad>
- https://twitter.com/MGIT_hyderabad

Membership of Professional Bodies:

1.Responsibilities Held at Institution Level: Placement ATPO 2.Studentbus incharge 3.Antirragging Committee 4.Counsellor.

Responsibilities Held at Department Level: Placement ATPO,Class incharge,Lab incharge,Mentor,NBA contributor,NAAC contributor

Research Guidance:1

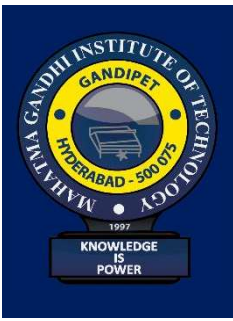
Honors/Awards Received:nil

Courses Handled at Under Graduate /Post Graduate Level:

- **UG:** VLSI Design ,Microprocessor and Microcontrollers,Computer Organization,Linear digital and integrated circuits.Digital Electronics,Low power vlsi design.
- **PG:nil**

Pubication:

1. Navaneetha, Alluri, and Kalagadda Bikshalu. "Reliability analysis of FinFET based high performance circuits." *Electronics* 12.6 (2023): 1407.
2. Krsihna, B. Vamsi, et al. "Design and development of graphene FET biosensor for the detection of SARS-CoV-2." *Silicon* (2022): 1-9.
3. Prakash, M. Durga, et al. "Performance analysis of ion-sensitive field effect transistor with various oxide materials for biomedical applications." *Silicon* (2021): 1-11.
4. Navaneetha, Alluri, and Kalagadda Bikshalu. "Reliability and Power Analysis of FinFET Based SRAM." *Silicon* 14.11 (2022): 5855-5862.
5. Navaneetha, Alluri, et al. "Performance evaluation of noise coupling on germanium based TSV filled material for future IC integration technique." *Materials Today: Proceedings* 45 (2021): 1494-1497.
6. Navaneetha, Alluri, and Kalagadda Bikshalu. "FinFET based Comparison analysis of power and delay of adder topologies." *Materials Today: Proceedings* 46 (2021): 3723-3729.
7. Navaneetha, Alluri, et al. "Analysis of nanowire FET over FinFET." *AIP Conference Proceedings*. Vol. 2492. No. 1. AIP Publishing, 2023.
8. Navaneetha, Alluri, and Kalagadda Bikshalu. "Exploration of Dynamic Power Dissipation of FinFET Based Multiplier." *NeuroQuantology* 20.7 (2022): 2264.
9. Navaneetha, Alluri, and Kalagadda Bikshalu. "Static Power Dissipation of FinFET Digital Circuit." *NeuroQuantology* 20.7 (2022): 2257.



ADDRESS:

- C – Block -Room No C-306

JNTUH ID:

93150404-145542

EMAIL:

anavaneetha_ece@mgit.ac.in

DATE OF JOINING:

10-10-2009

EXPERIENCE - 19 Years

- Teaching - 19
- Research -3
- Industry -
- Others –

SUMMARY:

- Publications - 11
- Conferences - 2
- Patents –
- Books –
- Honors/Awards -

EVENTS:

- Organized - 03
- Attended - 18

LET'S MEET ON SOCIAL:

- <https://www.facebook.com/mgithyderabad>
- <https://www.instagram.com/mgithyderabad>
- <https://www.linkedin.com/company/mgithyderabad>
- https://twitter.com/MGIT_hyderabad

1. **Conferences:**1.“Design and implementation of circuit using switched activity “,international conference on innovation CSE and information technology(ICICS – 2015).
2. “Implementation of high performance multiplier for portable devices”,(ICICS-2015)

Research & Consultancy:

1. nil

FDPs/STTPs

organised:

1. FDP on “Future Nanoelectronic Devices and Circuits”,from 06/07/20 to 10/07/20
2. FDP on Digital design using Verilog and VHDL in MGIT from 15/12/14 to 19/12/14.
3. One week Teaching and learning on “Microprocessor and Microcontroller”, Lab.

Refresher Courses/ Workshops/ Webinars/ Seminars/ Guest Lecture:nil

Events Attended

FDPs/STTPs:

- 1.Malaviya Mission Teacher Training Programme (MM-TTP) of University Grants Commission (UGC) Organized by UGC Malaviya Mission Teacher Training Centre, Jawaharlal Nehru Technological University Hyderabad from 16th January 2024 to 31st January 2024.
- 2.Faculty Development Program on Recent Trends in Nano electronics for Healthcare Applications in Electronics and Communication Engineering during 8th -12th April 2024 organized by Department of Electronics and Communication Engineering, IcfaiTech, IFHE, Hyderabad.
- 3.Effective” Technical report writing latex”, from 08/06/20 to 09/06/20, MGIT 2.One week online FDP on “recent trends in VLSI “,from 02/06/20 to 07/06/20 GRIET,Hyd-bad.
- 4.FDP on “Design implementation and verification” in VLSI sandeepani school of embedded design,bangalore from 27/04/20 to 01/05/20 ,Bangalore
- 5.FDP on “OP-AMP practical applications,design,simulation and implementation”,from 01/07/19 to 06/07/19,swayam nptel.
- 6.FDP on “ECAD and VLSI” from 08/07/19 to 12/07/19, MGIT.
- 7.FDP on “Nanodevices and Circuits”,17/06/10 to 22/06/19 ,NITW.
- 8.FDP on “Research Methodology” from 07/01/19 to 11/01/19,KU.
- 9.FDP on “MOS-AK conference”, from 25/02/2019 to 27/02/2019 ,IEEE Hyderabad with MOS-AK.
- 10.Summer Faculty Research Programme from 21/05/18 to 06/07/18 IIT Delhi.
- 11.FDP on “RF Antenna design” from 19/7/17 to 24/7/17,NITW.
- 12.Recent trends in “Digital image and Video processing”,from 22/2/16 to 27/2/16.
- 13.Workshop on Jeevam vidya in CBIT from 11/6/16 to 12/6/16.
- 14.Workshop on “socio-effectiveness of women_wow factor”,MGIT from 08/03/14 to 10/03/14.
- 15.FDP on “ECAD and VLSI Design”,in MGIT from 06/09/12 to 08/09/12.
- 16 . Workshop on Jeevanvidya in CBIT ,Hyd-bad from 01/06/21 to 12/06/21
17. Workshop on RF system design and testing in JNTUH on 21/12/11.
- 18.Refresher Courses/ Workshops/ Webinars/ Seminars/Guest Lecture: 1.RC in “Strategies for research oriented teaching and research “,in JNTUH from 19/5/14 to 07/6/14
- 19.Orientation course in OU from 03/06/13 to 01/07/13.

Online Certifications:nptel -2

Any Other Contribution:

1. Inhouse projects,Motivating students for higher education in premier institutions around the world,Motivating students in placements for preparing interviews in reputed companies,Gate coaching under prerana scheme,Reviewer for Materials today proceedings,Circuits and systems,international journal of electronic letters.